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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/819,895	03/29/2001	Mitsuru Mochizuki	205277US2	5372
22850 7	590 11/30/2004		EXAMINER	
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.			HA, DAC V	
	1940 DUKE STREET ALEXANDRIA, VA 22314		ART UNIT	PAPER NUMBER
	,		2634	

DATE MAILED: 11/30/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)		
Office Action Summary		09/819,895	MOCHIZUKI ET AL.		
		Examiner	Art Unit		
	·	Dac V. Ha	2634		
Period fo	The MAILING DATE of this communication app r Reply	ears on the cover sheet with the c	orrespondence address		
THE N - Exten after: - If the - If NO - Failur Any n	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. sions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period we to reply within the set or extended period for reply will, by statute, eply received by the Office later than three months after the mailing of patent term adjustment. See 37 CFR 1.704(b).	86(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days fill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONEI	nely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).		
Status					
1)🖂	Responsive to communication(s) filed on 09 Se	eptember 2004.			
		action is non-final.			
	, -				
Dispositi	on of Claims				
5)□ 6)⊠ 7)⊠	4) Claim(s) <u>1-9</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) <u>1-5</u> is/are rejected. 7) Claim(s) <u>6-9</u> is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.				
Application	on Papers				
9) 🗆 🗆	Γhe specification is objected to by the Examiner	•			
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.					
Priority u	nder 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachment	(s)				
1) D Notice	e of References Cited (PTO-892)	4) Interview Summary	PTO-413)		
3) 🔲 Inform	of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) No(s)/Mail Date	Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	te atent Application (PTO-152)		

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DETAILED ACTION

1. This office action is in response to the amendment filed on 09/09/04.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Mages et al. (US 6,178,313) (hereafter Mages).

Regarding claim 1, Mages teaches the followings:

"a power amplifier" (Figure 1, element 64; Col. 4, line 12);

"a variable gain amplifier connected in series with said power amplifier" (Figure 1, element 58; Col. 4, line 9);

"a gain controller configured to control a gain of said variable gain amplifier" (Figure 1, element 46; Col. 2, lines 49-50; Col. 4, lines 35-37);

Mages also suggests the teaching of the claimed subject matter "a bias voltage controller configured to control the bias voltage to said power amplifier" and "bias voltage control means for controlling the bias voltage of said power amplifier" in Figures 1, 2, element 46; Col. 2, lines 41-42, 51-52; Col. 4, lines 16-21, 60-65; Col. 5, lines 31-33, in that, the gain controller (Figure 1, element 46) teaches both the "apply means" and "control means". Moreover, Mages teaches the use of bias current. However, controlling the bias current is effectively controlling the (bias) voltage (Col. 4, lines 64-

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65). That is to say, controlling the power amplifier using a bias current or a "bias voltage" is essentially the same.

Mages further teaches the claimed subject matter "a gain compensation controller configured to compensate a gain variation of said power amplifier by inputting a control signal decided from the gain variation of said power amplifier to said variable gain amplifier for controlling the gain of said variable gain amplifier" in Figure 2, element 46; Col. 2, lines 38-45; Col. 4, lines 19-21, 38-41, 54-57, 60-62; Col. 7, lines 6-8.

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of the invention to optionally utilize either a bias current or bias voltage for the controlling the power amplifier.

Regarding claim 2, Mages further teaches the claimed subject matter "wherein said bias voltage controller controls the bias voltage of said power amplifier in response to desired output power of said power amplifier" in Col. 2, lines 40-45.

Regarding claim 3, Mages further suggests the teaching of the claimed subject matter "wherein said gain compensation controller comprises information about relationships between the desired output power of said power amplifier and the bias voltage of said power amplifier" in Figure 2, element 76; Col. 4, lines 45-50; Col. 2, lines 38-45; "and information about relationships between the bias voltage of said power amplifier and the gain said variable gain amplifier" (Col. 7, lines 44-50; Col. 2, lines 46-54; Col. 4, lines 54-57) in that, the bias current corresponds to the power control signal, the gain of the variable gain amplifier also corresponds to the same power control

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signal. Therefore, the gain of the variable gain amplifier also corresponds to the bias

current.

Regarding claim 4, Mages further suggests the teaching of the claimed subject matter "wherein the bias voltage of said power amplifier is varied at least at two steps"

in Col. 4, lines 30-34; Col. 8, lines 33-41, in that the bias (current) can be changed in

any steps.

Regarding claim 5, see claim 4.

Allowable Subject Matter

4. Claims 6-9 are objected to as being dependent upon a rejected base claim, but

would be allowable if rewritten in independent form including all of the limitations of the

base claim and any intervening claims.

Response to Arguments

5. Applicant's arguments filed on 09/09/04 have been fully considered but they are

not persuasive.

In the REMARKS of the amendment filed on 09/09/04, pages 5-7, applicants

have argued around claim 1, particularly, Mages does not teach "a gain compensation

controller configured to compensate a gain variation of said power amplifier by inputting

a control signal decided from the gain variation of said power amplifier to said variable

gain amplifier for controlling the gain of said variable gain amplifier".

Mages, however, teaches controlling the power amplifier including compensation

(for changes of the power amplifier) (col. 5, lines 10-13) by a combination of command

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from the controller (46) so that both the bias level and the gain of the power amplifier are controlled appropriately. Specifically, the gain of the power amplifier is effectuated via controlling the gain of the VGA (58). More particularly, the bias level applied to the power amplifier is derived from, at least in part, power control bits, which are used to generate signal indication for accessing the memory in the controller 46. The memory stores indications of levels of the power control signal and corresponding gain levels of the VGA, and the power control signal and corresponding bias level of the power amplifier (col. 7, lines 44-49). Thus, it can be seen that the gain levels of the VGA is "decided" from the variation of the power amplifier. Therefore, Mages still meets the requirement of claimed subject matter in claim 1.

Conclusion

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

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the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dac V. Ha whose telephone number is 571-273-3040. The examiner can normally be reached on 5/4.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Chin can be reached on 571-272-3056. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Dac V. Ha Examiner Art Unit 2634